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TERMINAL DISCLAIMER TO OBVIATE A DOUBLE PATENTING REJECTION OVER A "PRIOR" PATENT

03/25/8 04 FC:

Docket Number (Optional) NI MI POOSDO

TERMINAL TERMINAL	DISCLAIMER TO OBVIATE A DOUBLE PAT REJECTION OVER A "PRIOR" PATENT	TENTING	Docket Number (Optional) NLMI.P002DC
In re Application of: Sri	nivasan et al.		
Application No.: 10/743	,962	·	
Filed: December 22, 20	03		
For: Synchronous Con	tent Addressable Memory		
the expiration date of the and 173, and as the ter granted on the instant a	Microsystems. Inc, of, of, of, ow, the terminal part of the statutory term of any patent give full statutory term prior patent No. 6,697,911 m of said prior patent is presently shortened by any term application shall be enforceable only for and during such play patent granted on the instant application and is binding to	ranted on the instant as the term of said ninal disclaimer. The operiod that it and the p	prior patent is defined in 35 U.S.C. 154 owner hereby agrees that any patent so prior patent are commonly owned. This
would extend to the exp patent is presently show expires for failure is is held unenforcea		C. 154 and 173 of the	t granted on the instant application that prior patent, "as the term of said prior
is statutorily discla has all claims cand is reissued; or	a court of competent jurisdiction; imed in whole or terminally disclaimed under 37 CFR 1.32 seled by a reexamination certificate; erminated prior to the expiration of its full statutory term as		oy any terminal disclaimer.
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2. The undersign	ed is an attorney or agent of record. Reg. No. 40,171		
	Cl. 85 200		
	Ch EXC		March 22, 2005
	Signature		Date
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